

JAYPEE UNIVERSITY OF INFORMATION TECHNOLOGY, WAKNAGHAT

TEST - 1 EXAMINATION (Aug-Sept 2025)

B.Tech. - III Semester (CE)

COURSE CODE (CREDITS): 18B11MA311 (3)

MAX. MARKS: 15

COURSE NAME: NUMERICAL METHODS

COURSE INSTRUCTORS: RKB\*

MAX. TIME: 1 Hour

*Note: All questions are compulsory. Use of scientific calculator is allowed. The candidate is allowed to make suitable numeric assumptions wherever required for solving problems*

*... assumptions wherever required for solving problems*

Q.No	Question	CO	Marks																
Q1	Suppose that we are using a computer with a fixed word length of 4 digits. Evaluate the error due to chopping in representing the number 86.45315.	CO-1	2																
Q2	If the true value of an observation is 1565 and the approximated value is 1520, then find the percentage relative error.	CO-1	2																
Q3	If for a transistor, the node-voltage equation reduces to a polynomial like $x^3 - x - 4 = 0$ after non-dimensionalization. Solve the polynomial using Newton-Raphson method correct to 3 decimal places to find the node voltage (normalized) at the DC operating point needed before analyzing small-signal behavior or designing bias networks.	CO-2	4																
Q4	<p>Suppose we have a <b>gate delay</b> measured at different input transition times. The measured gate delays at three input slews are given:</p> <table border="1" style="margin: 10px auto;"> <tr> <td>Input Slew (ns)</td> <td>0.1</td> <td>0.3</td> <td>0.5</td> </tr> <tr> <td>Delay (ns)</td> <td>0.25</td> <td>0.40</td> <td>0.65</td> </tr> </table> <p>Using Lagrange Interpolation, estimate the interpolated delay at input slew 0.20.</p>	Input Slew (ns)	0.1	0.3	0.5	Delay (ns)	0.25	0.40	0.65	CO-2	4								
Input Slew (ns)	0.1	0.3	0.5																
Delay (ns)	0.25	0.40	0.65																
Q5	<p>Suppose the following data represents the measured NMOS saturation current (<math>I_d</math>) versus gate voltage (<math>V_{gs}</math>) data:</p> <table border="1" style="margin: 10px auto;"> <tr> <td><math>V_{gs}</math></td> <td>0.8</td> <td>1.0</td> <td>1.2</td> <td>1.4</td> <td>1.6</td> <td>1.8</td> <td>2.0</td> </tr> <tr> <td><math>I_d</math></td> <td>0.05</td> <td>0.40</td> <td>1.10</td> <td>2.10</td> <td>3.45</td> <td>5.10</td> <td>7.05</td> </tr> </table> <p>Using the method of least square, fit a linear order equation to interpolate the value of saturation current at the gate voltage 1.5.</p>	$V_{gs}$	0.8	1.0	1.2	1.4	1.6	1.8	2.0	$I_d$	0.05	0.40	1.10	2.10	3.45	5.10	7.05	CO-2	3
$V_{gs}$	0.8	1.0	1.2	1.4	1.6	1.8	2.0												
$I_d$	0.05	0.40	1.10	2.10	3.45	5.10	7.05												

\*\*\*

\*\*\*