

JAYPEE UNIVERSITY OF INFORMATION TECHNOLOGY, WAKNAGHAT
TEST -1 EXAMINATION- Sep 2017

B.Tech(ECE) V Semester

COURSE CODE: 10B11CI401

MAX. MARKS: 15

COURSE NAME: Microprocessors and Controllers

COURSE CREDITS: 4

MAX. TIME: 1 HR

Note: All questions are compulsory. Carrying of mobile phone during examinations will be treated as case of unfair means.

Q.1. [3 Marks. Each question is half mark]

- a) Explain the machine level specifications for 1 and 0 for TTL digital devices.
- b) What is noise immunity of 8086 microprocessor?
- c) List steps in execution of the instruction MOV AX, GREEN.
- d) What is MAXIMUM mode operation?
- e) Describe the signals READY and BHE.
- f) Explain the ideal specification of memory for 8086 processor at 5Mhz.

Q.2. [3 marks] Describe the memory operations using timing state diagrams and showing all control signals.

Q.3. [3 marks] Explain with example the data addressing modes for 8086.

Q.4. [3 marks] Draw the register architecture of 8086 and explain the functions of Control Registers.

Q.5. [3 marks] What is the need for Buffering and Latching? How is it achieved for a 8086 CPU.

CT-7, BT