

JAYPEE UNIVERSITY OF INFORMATION TECHNOLOGY, WAKNAGHAT

TEST -1 EXAMINATION- September 2016

B.Tech CSE&IT 7th Semester

COURSE CODE: 13B1WCI731

MAX. MARKS: 15

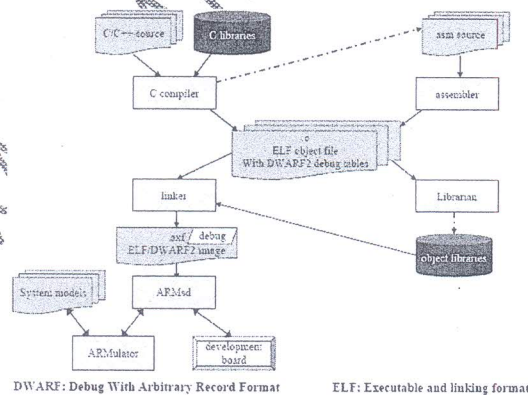
COURSE NAME: Arm Based Embedded System Design

COURSE CREDITS: 03

MAX. TIME: 1 HRS

Note: All questions are compulsory. Carrying of mobile phone during examinations will be treated as case of unfair means. All questions carry equal marks.

1. List various classes of ARM cores architecture with application domains
 - a. Defense.
 - b. Space.
 - c. Domestic.
 - d. Industrial.
 - e. Health and Education.
2. Explain the various ARM development tools



3. Give the major differences between RISC and CISC processors in tabular format.
4. Draw and explain the register organization of ARM architecture with seven basic operating modes
5. Explain the various parts of Program Status Registers

